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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE  
PATENT APPLICATION

In re Application of:

Efland	Atty. Docket: TI-31678
Serial No.: 09/975,630	Art Unit: 2826
Filed: 10/12/2001	Examiner: L. Andujar
For: CIRCUIT STRUCTURE INTEGRATING THE POWER DISTRIBUTION FUNCTIONS OF CIRCUITS AND LEADFRAMES INTO THE CHIP SURFACE	

Commissioner for Patents  
P. O. Box 1450  
Alexandria, VA 22313-1450

CERTIFICATE OF MAILING 37 CFR §1.8(a)

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, Alexandria, VA 22313-1450 on the date indicated below.

7-21-2005  
*Jackie McBride*  
Jackie McBride

**LETTER TO THE OFFICIAL DRAFTSPERSON**

Sir:

In response to the Notice Regarding Drawings mailed July 8, 2005, please find enclosed one (1) sheet of formal drawings as required by said Notice. This sheet of drawings, which includes Figures 1 and 2, has been labeled as "Replacement Sheet" and should replace the original sheet including Figures 1 and 2. It is noted that item 10 in the Notice of Draftperson's Patent Drawing Review indicated that the lines, numbers, and letters of Figures 3, 4, and 5 were of poor quality. However, this application does not now and has never contained Figures 3, 4, and 5.

Respectfully submitted,

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